



Attorney Docket No.: MTEC-005/00US  
 Client Reference No.: 0122.00US

PATENT

Express Mail Label Number: EV 525580137 US  
 Date of Deposit: May 17, 2005

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of THEKKATH

Serial No.: 09/844,672

Examiner: Khatri, Anil

Confirmation No.: 8987

Art Unit: 2124

Filed: April 30, 2001

For: **DYNAMIC SELECTION OF A COMPRESSION ALGORITHM FOR TRACE DATA**

**Mail Stop RCE**  
 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT TRANSMITTAL**

Enclosed is an Information Disclosure Statement and accompanying Form PTO/SB/08A for the above-identified patent application.

In accordance with 37 C.F.R. §1.97(b), no additional fee for submission of the IDS is required.

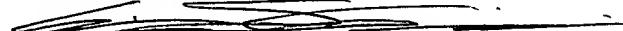
The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§1.16, 1.17, and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 03-3117.

Dated: May 17, 2005

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 3000 El Camino Real  
 Palo Alto, CA 94306-2155  
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By:

Respectfully submitted,  
**COOLEY GODWARD LLP**

  
 William S. Galliani  
 Reg. No. 33,885

WSG:dm

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**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. §1.97(b)**

In accordance with the duty of disclosure set forth in 37 C.F.R. §1.56, Applicant(s) hereby submits the following information in conformance with 37 C.F.R. §§1.97 and 1.98.

Pursuant to 37 C.F.R. §1.98, a copy of each document cited in the attached Form PTO/SB/08 is enclosed.

This Information Disclosure Statement is filed within any one of the following time periods:

- within three months from the filing date of this national application other than a CPA under 37 C.F.R. § 1.53(d);
- within three months from the date of entry of the national stage as set forth in 37 C.F.R. §1.491 in this international application;
- before the mailing date of a first office action on the merits; or
- before the mailing of a first office action after the filing of a request for continued examination under 37 C.F.R. § 1.114.

[X] The use of a compression algorithm on trace data is known. The inventor has not been able to identify a commercial product implementing such a technique, but believes that there are commercial products that use a fixed compression algorithm on trace data. Any such technique stands in contrast to the claimed dynamic selection of a compression algorithm for trace data. For example, claim 1 recites "a plurality of compression modules" and "a compression selector module", components that are not shown or suggested by the use of a static compression algorithm on trace data.

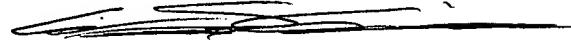
It is respectfully requested that the Examiner consider the above-noted information and return an initialed copy of the attached Form PTO/SB/08A to the undersigned.

Dated: May 17, 2005

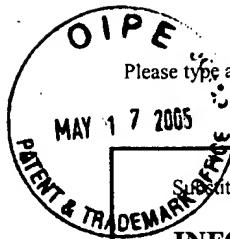
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PTO/SB/08A (08-00)

Substitute for form 1449A/PTO		<i>Complete if Known</i>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>		Application Number	09/844,672
		Filing Date	April 30, 2001
		First Named Inventor	THEKKAT
		Group Art Unit	2124
		Examiner Name	Khatri, Anil
Sheet	1 of 1	Attorney Docket Number	MTEC-005/00US

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY
		Office <sup>1</sup>	Number <sup>2</sup>	Kind Code <sup>3</sup> (if known)		

OTHER - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	D1	Susan J. EGGLERS, et al., "Techniques for Efficient Inline Tracing on a Shared-Memory Multiprocessor," University of Washington, 1990 ACM, pp. 37-47.	
	D2	Richard A. UHLIG, et al., "Trace-Driven Memory Simulation: A Survey," Intel Microcomputer Research Lab; University of Michigan, ACM Computing Surveys, Vol. 29, No. 2, June 1997, pp. 128-170.	
	D3	Eric ROTENBERG, et al., "Trace Processors," University of Wisconsin, 1997 IEEE Service Center, 12 pp.	
	D4	E.N. ELNOZAHY, "Address Trace Compression Through Loop Detection and Reduction," ©1999 ACM 1-58113-083, pp. 214, 215.	
	D5	Darren JONES, "Opal Coprocessor Interface," MIP Proprietary/Confidential, June 4, 1999, pp. 1-18.	
	D6	Darren JONES, MIPSS64™ 5KC™ Processor Cores User's Manual, Rev. 1.0, July 4, 1999, pp. 6-1 to 6-26.	
	D7	MIPSS64™ 5KC™ Processor Cores User's Manual, Rev. 1.0.1., 1999, pp. 247-276.	

Examiner Signature		Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3).

<sup>2</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

<sup>3</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible.

<sup>4</sup> Applicant is to place a check mark here if English language Translation is attached.

<sup>1</sup> Unique citation designation number.

<sup>2</sup> Applicant is to place a check mark here if English language Translation attached.